

## AN MOPA RF SYSTEM OF INS SF CYCLOTRON

K. Sato, M. Fujita, N. Yamazaki, T. Honma, Y. Ohshiro, T. Tanabe and M. Sekiguchi

### Introduction

The existing rf system of the INS SF cyclotron employs a self-excited oscillator<sup>1)</sup> and supplies rf power to the dee in the frequency range of 7.3 to 17.5 MHz. There has been a constant demand for extended frequency up to 22.5 MHz. The maximum rf power, which excites the rf dee voltage up to 85 KV at a frequency of 20.5 MHz, is estimated to become 150 KW. A master oscillator and power amplifier (MOPA) rf system was proposed and its construction was begun last year. A new rf system is also expected to perform improved stability, improved reliability and simplification of operation procedures, especially in changing an operation frequency.

A test bench has been constructed and a preliminary performance of the whole system is being studied.

### MOPA rf System

An MOPA rf system, shown in Fig. 1, features a final power amplifier (PA), an rf wideband driver amplifier, current controlled attenuators and a master oscillator (MO), rf dee voltage regulation loop and automatic rf phase regulation loop. A personal computer is used for automated settings in interfacing with digital instruments through IEC-IB.

### Final Power Amplifier

For our final power amplifier, we choose a plate tank of the coaxial resonator of  $1/4 \lambda$  mode, an all pass network of a grid rf circuit and the RCA 4648 tetrode. A plate tank is very flexible for adjusting the plate load impedance. A  $50 \Omega$ , 50 KW water cooled resistor is coupled to the PA plate tank via 18 pF capacitor. Whenever the cyclotron resonator is detuned, this load absorbs an excess rf power and protects against break-down in the plate tuning capacitor and the rf coupling capacitor between the PA and the main resonator. An all pass network of a bridged-tee configuration, shown in Fig.1, has been successfully developed at the ORIC<sup>2)</sup> and eliminates most circuit tuning in frequency range of 7 to 23 MHz. This circuit dissipates an rf power of four times in  $\pi$ -network for the same rf grid voltage and the high gain characteristics of a power tube is then desired. The 4648 has a gain superior to other power tubes. Besides, the 4648 has the low feedback capacitance of 0.6 pF and is expected to be operated without neutralization in our frequency range. The plate tank is copied from the ORIC one.<sup>3)</sup> A movable short and four vacuum variable capacitors in the plate tank are driven with powerful pulse motors. A screen bypass is disc capacitors assembled as double-decker Kapton sandwich, in which a 3 mil Kapton film is used.

A plate load impedance is chosen to be  $350 \Omega$ . Settings of dc power supplies are designed to be 14.5 KV in plate p.s., 1100 V in screen p.s. and -110 V in control grid p.s.. The PA output power of 150 KW is estimated to be achieved in an rf grid voltage of  $75 \text{ V}_{\text{r.m.s.}}$ .

### Phase Regulation Loop

The rf signals from capacitive pick up in the grid, plate and dee are converted into 455 kHz signals in I.F. amplifier in which the superheterodyne method is used. The phase between 455 kHz signals is measured with a phase-meter and its analog output drives a pulse motor through a drive unit. An automatic tuning system demodulates a phase between the grid and dee signals, and a phase between the grid and plate signals, separately.

### Dee Voltage Regulation Loop

A double pick up dee voltage detector, which has been developed at the RCNP AVF cyclotron, is used. This can generate an exact dee voltage signal

